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IBM MICROELECTRONICS
INTELLECTUAL PROPERTY LAW
1000 RIVER STREET
972 E
ESSEX JUNCTION, VT 05452

CONFIRMATION NO. 4737

FILING RECEIPT



Date Mailed: 08/18/2008

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Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 24241

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/16296 11/04/2004

Foreign Applications JAPAN 2003-377485 11/06/2003

If Required, Foreign Filing License Granted: 08/14/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US 10/595.742

Projected Publication Date: 11/27/2008

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor Storage Device and Burst Operation Method

Preliminary Class

365

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